DECLARATION AND POWER OF ATTORNEY FOR U.S. PATENT APPLICATION

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	entor (if plural inventors are na	I hereby declare that: my resident in the original, first and solutions are below) of the subject materials.				
Title:	Semiconductor	Device Package	Manufacturin	g Method and		
	Semiconductor	Device Package	Manufactured	by the Metho	od	
Semiconductor Device Package Manufactured by the Method of which is described and claimed in:) the attached specification, or) the specification in the application Serial No						
	COUNTRY	APPLICATION NO). DA	TE OF FILING	PRIORITY CLAIMED	
Ja	pan	11-202847	July :	16, 1999	YES	
Ja	pan	2000-63686		8, 2000	YES	
		,				
he first ped in Title CT inte	paragraph of Title 35, United S	35, United States Code, §120 f this application is not dislcose States Code, §112, I acknowled ations, §1.56 which occurred loplication. U.S. FILING	of in the prior United Sign the duty to disclose between the filing date	States application in the information material to a	manner provided by atentability as definant the national or ED, PENDING,	

And I hereby appoint John T. Miller, Reg. No. 21,120; Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Jeffrey Nolton, Reg. No. 25,408; Warren M. Cheek, Jr., Reg. No. 33,367; Nils E. Pedersen, Reg. No. 33,145 and Charles R. Watts, Reg. No. 33,142, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., attorneys to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys named herein to accept and follow instructions from _______ as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

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Post Office Address	ADDRESS	CITY	STATE OR COUNTRY	ZIP CODE		
Full Name of Seventh Inventor	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME			
Residence & Citizenship	CITY	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP			
Post Office Address	ADDRESS	CITY	STATE OR COUNTRY	ZIP CODE		

I further declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Norihito TSUKAHARA Ind Inventor Takashi AKIGUCHI Hidenori MIYAKAWA Ith Inventor The above application may be more particularly identified as follows: The above application Serial No. Applicant Reference Number 534653 Atty Docket No. Semiconductor Device Package Manufacturing Method and Semiconductor Device Package Manufactured by the Method Interval Application Date The Morihito TSUKAHARA Takashi Akiguchi Date 22/11/200/ Date 22/11/200/ Date 32/11/200/ Date 32/11/200/ Date 32/11/200/ Date 34/11/200/ Date	st Inventor	noce	rila x	sukahar	<u>ca</u>	Date	20/11/2001	
The above application may be more particularly identified as follows: U.S. Application Serial No. 10/031,000 Filing Date January 16, 2002 Applicant Reference Number 534653 Atty Docket No. 2002-0024A Title of Invention Semiconductor Device Package Manufacturing Method and	NOT1 2nd Inventor Taka	nito TSUKAHARA	bashi	Abigna	hi	Date	22/11/2001	
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The above application may be more particularly identified as follows: U.S. Application Serial No	5th Inventor					Date		
The above application may be more particularly identified as follows: U.S. Application Serial No	6th Inventor					Date		
The above application may be more particularly identified as follows: U.S. Application Serial No	7th Inventor					Date		
Applicant Reference Number 534653 Atty Docket No. 2002-0024A Title of Invention Semiconductor Device Package Manufacturing Method and							•	
Title of Invention Semiconductor Device Package Manufacturing Method and	U.S. Application Ser	rial No.	10/031,	000	Filing Date	January	16, 2002	
Tide of Invention Semiconductor Device Package Manufacturing Method and Semiconductor Device Package Manufactured by the Method	Applicant Reference	Number <u>534653</u>		At	ty Docket No)	2002-0024A	
Semiconductor Device Package Manufactured by the Method	Title of Invention	Semiconductor	Device	Package	Manufac	turinc	Method and	
		Semiconductor	Device	Package	Manufac	ctured	by the Method	l